Docket No.: H0782

## DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

My residence, post office address, and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter claimed and for which a patent is sought on the invention entitled:

## TEST STRUCTURE AND METHOD FOR FAILURE ANALYSIS OF SMALL CONTACTS IN INTEGRATED CIRCUIT TECHNOLOGY DEVELOPMENT

the specification of									
is attached here	to								
was filed on as Application Serial No and was amended on (if applicable).									
I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above. I do not know and do not believe the same was ever known or used in the United States of America before this invention thereof or more than one year prior to this application, and that the same was not in public use or on sale in the United States of America more than one year prior to this application. I acknowledge the duty to disclose information which is known to me to be material to patentability in accordance with title 37, Code of Federal Regulations section 1.56.									
Prior Foreign App	olication(s):								
	_		Priority Claimed						
Number	Country	Day/Month/Year filed	<u>Yes</u>	<u>No</u>					
I hereby claim the lapplication(s) listed		USC 119(e) of any United Stat	es provisio	onal					
<b>Prior Provisional</b>	Application(s):								
Application Numb	oer <u>Filin</u>	g Date							
States application (states, lister application is not commanner provided)	s), or Section 36 and below and, in disclosed in the para by the first para uty to disclose m	Title 35, United States Code, 5(c) of any PCT international sofar as the subject matter of rior United States or PCT integraph of Title 35, United Staterial information as defined	application each of the each o	n designating the claims of application 1.  Section 1.	ng the of this in the 112, I				

the national or PCT international filing date of this application:

Docket No.: H0782

## **Prior U.S. Application(s):**

Serial No.

Filing Date

Status: Patented, Pending, Abandoned

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

I hereby revoke any previous Powers of Attorney and appoint the following attorney(s) and/or agent(s), each said individual being a member or associate of The Law Offices of Mikio Ishimaru or being employed by Advanced Micro Devices, Inc.:

Mikio Ishimaru	Reg. No. 27,449	Kent Cooper	Reg. No. 37,296
Elizabeth A. Apperley	Reg. No. 36,428	Paul S. Drake	Reg. No. 33,491
Bradley Botsch	Reg. No. 34,552	Keith Witek	Reg. No. 37,475
Michael Caywood	Reg. No. 37,797	Harry A. Wolin	Reg. No. 32,638
Daniel R. Collopy	Reg. No. 33,667	William D. Zahrt II	Reg. No. 26,070

for so long as they remain with such law offices or company with full power of substitution and revocation, to prosecute this application and to transact in connection therewith all business in the Patent and Trademark Office and before competent International Authorities; said appointment to be to the exclusion of myself and my other attorney(s); and all future correspondence should be addressed to:

Mikio Ishimaru
The Law Offices of Mikio Ishimaru
1110 Sunnyvale – Saratoga Rd., Suite A1
Sunnyvale, California 94087

Inventor's signature:	/ \ •			0 \	_
Inventor's signature:	Day	y ( . News	Quanta Date	e: Feb 24	2004

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